

S5	959	(710/260).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 07:56
S6	17342	(reduced or low adj latency) and (interrupt) and microprocessor	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 07:59
S7	1457	S6 and ("710"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 07:59
S8	22	(interrupt adj vector adj table) and (interrupt adj latency)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/27 08:03
S9	7	("5968159").URPN.	USPAT	OR	OFF	2005/05/25 10:55
S10	1	("5944840").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 11:32
S11	0	(interrupt adj latency) and (memory adj controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 11:35
S12	126	(interrupt adj latency) and (memory adj controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 11:53
S13	0	(interrupt adj latency) and (re-addressing)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 11:53
S14	1	(interrupt adj latency) and (re-addressing)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 14:00
S15	1	("6317827").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 12:17
S16	3	("6317827").URPN.	USPAT	OR	OFF	2005/05/25 12:17
S17	11	("5210854" "5361343" "5388267" "5421017" "5530847" "5748940" "5864698" "5872967" "5896566" "6009495" "6085299").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/05/25 12:29
S18	78	("5210854").URPN.	USPAT	OR	OFF	2005/05/25 12:31

S19	6	("5737585").URPN.	USPAT	OR	OFF	2005/05/25 13:34
S20	126	(interrupt adj latency) and (memory adj controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 14:40
S21	11	(re-addressing) and (memory adj controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 14:44
S22	741	(interrupt adj service) and (memory adj controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 15:35
S23	928	(711/113).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 15:38
S24	772	(711/206).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 15:39
S25	0	("19andinterrupt").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 15:39
S26	193	S24 and interrupt	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 15:40
S27	3	S24 and (interrupt adj latency)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 15:43
S28	169	(711/221).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 15:44
S29	29	S28 and interrupt	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 15:49
S30	80	(711/215).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 15:53
S31	14	(interrupt adj service adj routine) same (memory adj controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 15:59

S32	3	(interrupt adj service adj routine) same (high adj speed adj memory)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 16:05
S33	704	(interrupt adj latency)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/25 16:05
S34	1	("5548730").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 08:06
S35	80	("5548730").URPN.	USPAT	OR	OFF	2005/05/26 08:06
S36	17355	(reduced or low adj latency) and (interrupt) and microprocessor	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/27 08:03
S37	1459	S36 and ("710"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/27 08:03
S38	97	S37 and arm	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/27 08:03